

**TECHNICAL WORK MAY NOT BEGIN PRIOR TO GO APPROVAL**

NASA/GODDARD SPACE FLIGHT CENTER

**REQUEST FOR TASK PLAN / TASK ORDER**

CONTRACTOR	CONTRACT NO./TASK NO.	JOB ORDER NUMBER	APPROP. FY
QSS Group, Inc.	NAS5- 99124      TASK NO. <b>427</b>	AMENDMENT <del>410-287-17-10-89</del> <del>743-953-20-49-89</del>	00 99

TASK TITLE: (NTE 80 characters; include Project name)  
**MAP Umbilical Console FPGA modifications**

**APPROVALS:** (Type or print name and sign)

ASSISTANT TECHNICAL REPRESENTATIVE (OR TASK MONITOR)	DATE	ORG CODE	MAIL CODE	PHONE
Kurt Rush <i>Kurt Rush</i>	12-6-00	566	566	301-286-5418 <sup>1196</sup>
BRANCH HEAD	DATE	CODE	PHONE	
James Stephens <i>James Stephens</i>	12/6/00	566	301-286-8380	
CONTRACTING OFFICER'S TECHNICAL REPRESENTATIVE (COTR)	DATE	CODE	PHONE	
for Robert S. Lebair <i>Robert S. Lebair</i>	12/6/00	560	301-286-6588	

FLIGHT HARDWARE, CRITICAL GSE OR SOFTWARE? <small>(IF YES, NEED CODE 303 CONCURRENCE NEXT BLOCK)</small>	CONTRACTING OFFICER'S QUALITY REP.	DESIGNATED FAM:
<input checked="" type="checkbox"/> NO <input type="checkbox"/> YES		

The contractor shall identify and explain the reason for any deviation or conditional assumptions taken with respect to this Task Order or any of the technical requirements of the Task Order Statement of Work and related specifications. The contractor shall complete and submit the required Reqs. ar. Certs.

*(To be completed by Contracting Officer)*  
**C.O. Requested Quote on:**  
**Date: DEC 7 2000**

Contractor will develop specification or statement of work under this task for a future procurement.  NO  YES

Flight hardware will be shipped to GSFC for testing prior to final delivery.  NO  YES  N/A

Government Furnished Property/Facilities:  NO  YES -- SEE LIST OF GFP (offsite only) / FACILITIES (onsite only)

Onsite Performance:  NO  YES      If yes:  TOTAL  PARTIAL  
If partial, indicate onsite work in SOW by asterisk (\*)

Surveillance Plan Attached:  NO  YES

Highlighted Contract Clauses: *(to be completed by Contracting Officer)*

**INCENTIVE FEE STRUCTURE (check one)**  
(See Contract NAS5-99124, Attachment K, Incentive Fee Plan)

	No. 1	No. 2	<input checked="" type="checkbox"/> No. 3	No. 4	No. 5
Cost	10%	50%	25%	25%	%
Schedule	15%	25%	25%	50%	%
Technical	75%	25%	50%	25%	%

*(To be completed by Contracting Officer)*

The target cost of this task order is \$ \_\_\_\_\_.

The target fee of this task order is \$ \_\_\_\_\_.

The total target cost and target fee of this task order as contemplated by the Incentive Fee clause of this contract is \$ \_\_\_\_\_.

The maximum fee is \$ \_\_\_\_\_.

The minimum fee is \$0.

**AUTHORIZED SIGNATURE:** \_\_\_\_\_

THIS TASK ASSIGNMENT IS ISSUED ACCORDING TO THE CONTRACT CLAUSE "TASK ASSIGNMENT PLAN PARTS"

SIGNATURE OF CONTRACTING OFFICER \_\_\_\_\_ DATE \_\_\_\_\_ TYPED NAME OF CONTRACTING OFFICER \_\_\_\_\_

**CONTRACTOR'S ACCEPTANCE:**

\_\_\_\_\_ DATE \_\_\_\_\_

AUTHORIZED SIGNATURE      DATE

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QSS Group, Inc.	NAS5- 99124	427	

Applicable paragraphs from contract Statement of Work:

**STATEMENT OF WORK:** (Continue on blank paper if additional space is required)

Contractor shall modify an existing FPGA circuit design to synchronize asynchronous input signals.  
Contractor shall load the modified design onto a Code 566 supplied memory device via Code 566 equipment.  
Contractor shall install the memory device and test the modified FPGA design.  
Modifications should be done by original designer of the FPGA

**PERFORMANCE SPECIFICATIONS:**

The FPGA design shall conform to Goddard Directive No. 561-PG-8700.2.1 for Flight FPGA Design Guidelines.  
Perform design using ViewLogic.

**Technical Progress Report:** Acceptable performance is that the ATR is satisfied that he is being kept informed of the status of work performed and of issues requiring his attention. Report to include: (1) summary of monthly progress; (2) plans for next month; (3) problems; (4) issues; and (5) resolution of problems/issues.

**Management:** Performance will be measured against the following metrics: (1) accomplishment of objectives; (2) clear, incremental progress; (3) responsiveness to issues; (4) efficient and appropriate staffing; and (5) coordination with and good working relationship with ATR and other related contractor efforts, if applicable.

**APPLICABLE DOCUMENTS:**

Goddard Directive No. 561-PG-8700.2.1

**TASK END DATE:** 12/15/00

**MILESTONES/DELIVERABLES AND DATES:**

- |   |  |
|---|--|
| 1. New FPGA design installed and tested                             | 12/15/00                               |
| 2. Printed and electronic copies (ViewLogic) of the updated circuit | 12/15/00                               |
| 3. Technical Progress Report  | Monthly, 15th of the month<br>12/15/00 |

**PERFORMANCE STANDARDS:**

**Schedule:** On-time delivery/completion of the deliverables/milestones  
**Technical:** ATR's acceptance of the above

**FINAL DELIVERY DESTINATION (NAME, BLDG, ROOM):**

Kurt Rush, building 29, room 360